



Docket No.: INF-P80224 US

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By: Jm P.

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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant : Gerhard Beitel et al.
Applic. No. : 09/734,467
Filed : December 11, 2000
Title : Methods for Producing a Structured Metal Layer

INFORMATION DISCLOSURE STATEMENT

Hon. Commissioner of Patents and Trademarks,
Washington, D.C. 20231

Sir:

In accordance with 37 C.F.R. 1.98 copies of the following patents and/or publications are submitted herewith:

United States Patent No. 5,142,437 (Kammerdiner et al.), dated August 25, 1992;

United States Patent No. 5,348,894 (Gnade et al.), dated September 20, 1994;

United States Patent No. 5,374,578 (Patel et al.), dated December 20, 1994;

United States Patent No. 5,471,364 (Summerfelt et al.), dated November 28, 1995;

United States Patent No. 5,504,041 (Summerfelt et al.), dated April 2, 1996;

United States Patent No. 5,555,486 (Kingon et al.), dated September 10, 1996;

United States Patent No. 5,708,302 (Azuma et al.), dated January 13, 1998;

United States Patent No. 5,723,171 (Cuchiaro et al.), dated March 3, 1998;

United States Patent No. 5,789,268 (Chivukula et al.), dated August 4, 1998;

German Published, Non-Prosecuted Patent Application DE 199 04 781 A1 (Ochiai, et al.), dated August 12, 1999, dielectric capacitor, method for producing said capacitor and dielectric memory having said capacitor.

If no translation of pertinent portions of any foreign language patents or publications mentioned above is included with the aforementioned copies of those applications, patents and/or publications, it is because no existing translation is readily available to the applicant.

Respectfully submitted,

Mark P. Weichselbaum
Reg. No. 43,248



For Applicants

Date: 3 / 1 / 01

Lerner And Greenberg, P.A.
Post Office Box 2480
Hollywood, FL 33022-2480
Tel: (954) 925-1100
Fax: (954) 925-1101

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